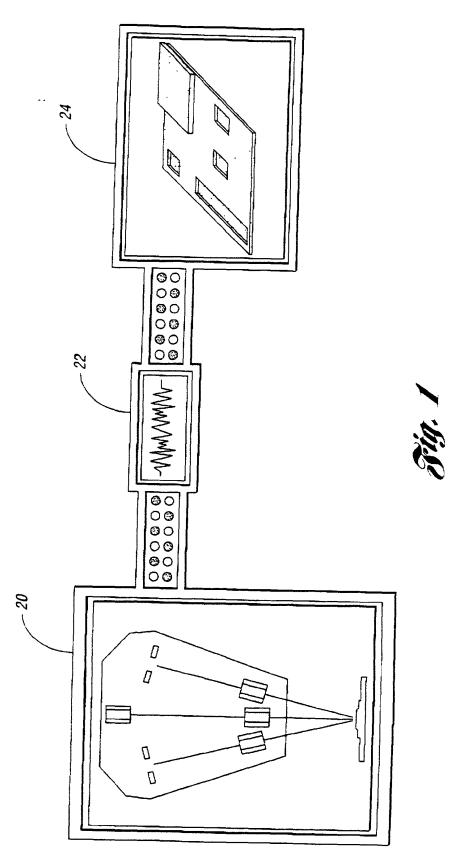
Title: Method and System For Inspecting Electronic Components Mounted on Printed Circuit Boards First Named Inventor: John J. Weisgerber, et al.

Application Serial No.:

/ Atty. Docket No.: GSIL 0148 PUS

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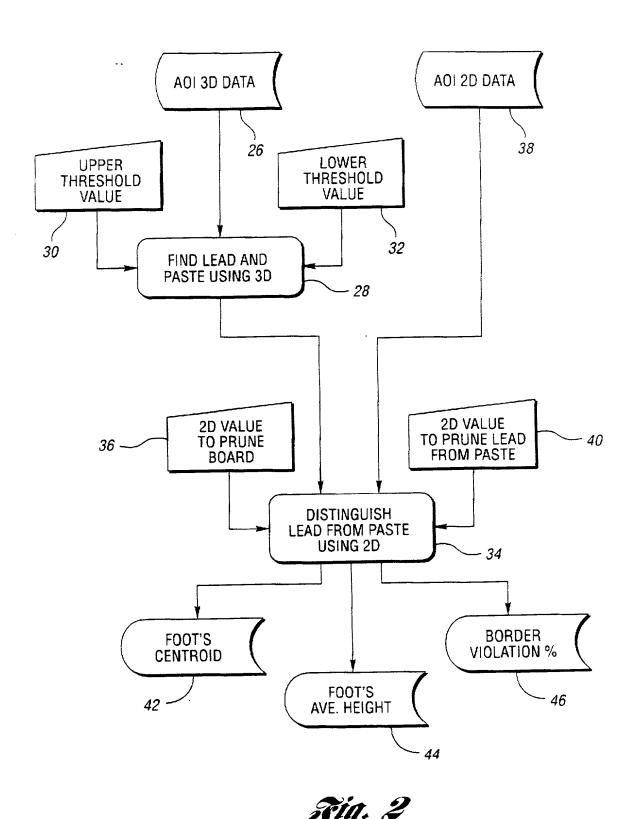
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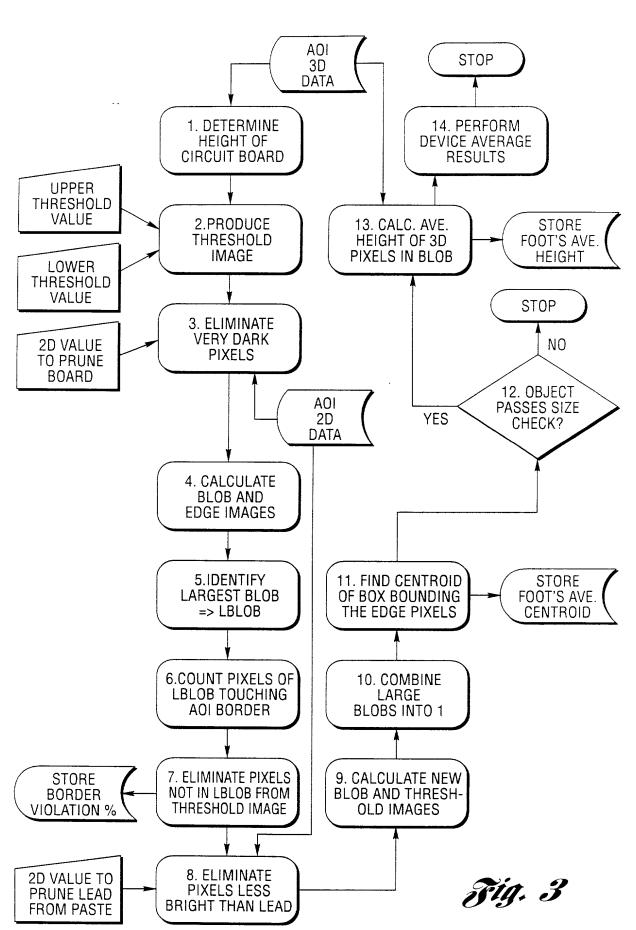
/ Atty. Docket No.: GSIL 0148 PUS

2/9



/ Atty. Docket No.: GSIL 0148 PUS

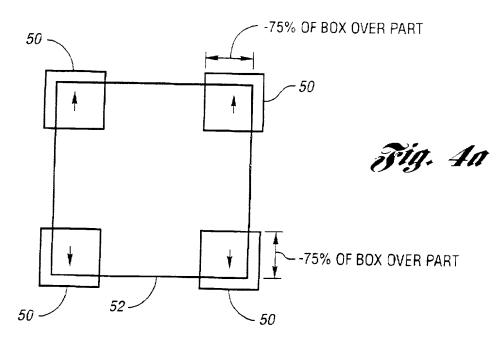
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4/9



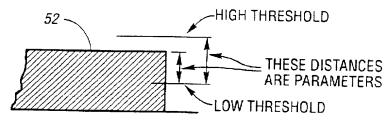


Fig. 46

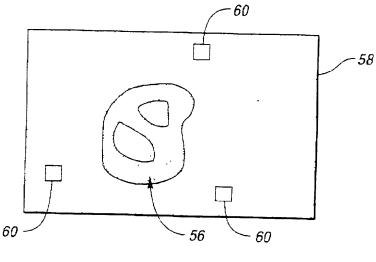


Fig. 5a

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/ Atty. Docket No.: GSIL 0148 PUS

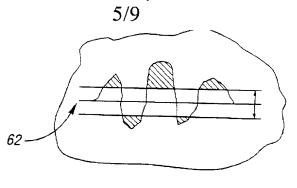
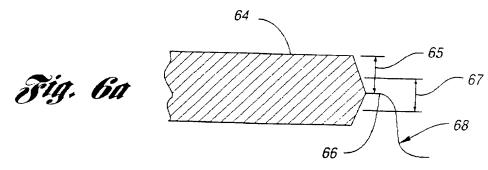
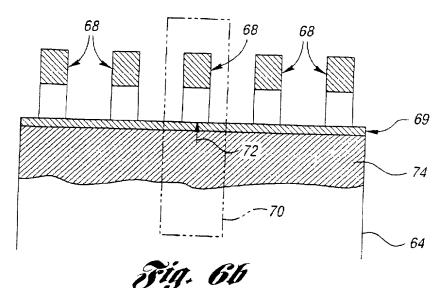
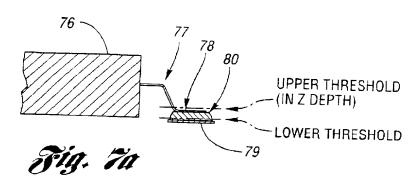


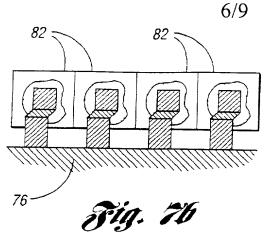
Fig. 56

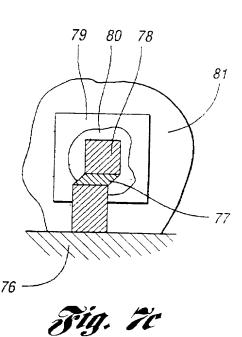






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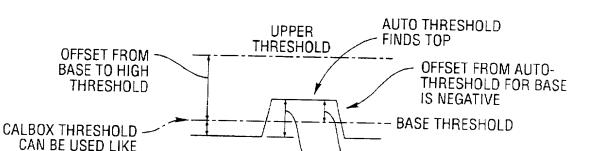


Fig. 8

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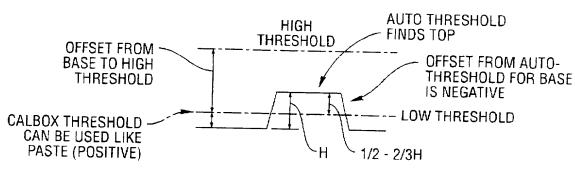


Fig. 9n

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Title: Method and System For Inspecting Electronic Components Mounted on Printed Circuit Boards
First Named Inventor: John J. Weisgerber, et al.

Application Serial No.: / Atty. Docket No.: GSIL 0148 PUS

Application Serial No.:

7/9

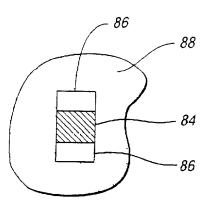
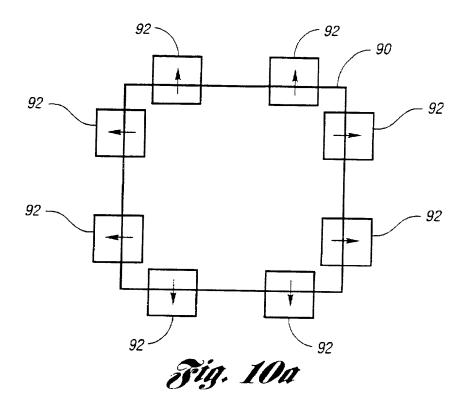


Fig. 96



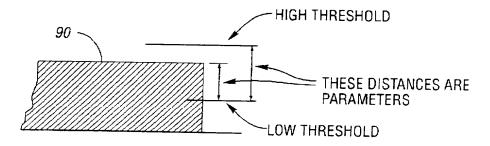
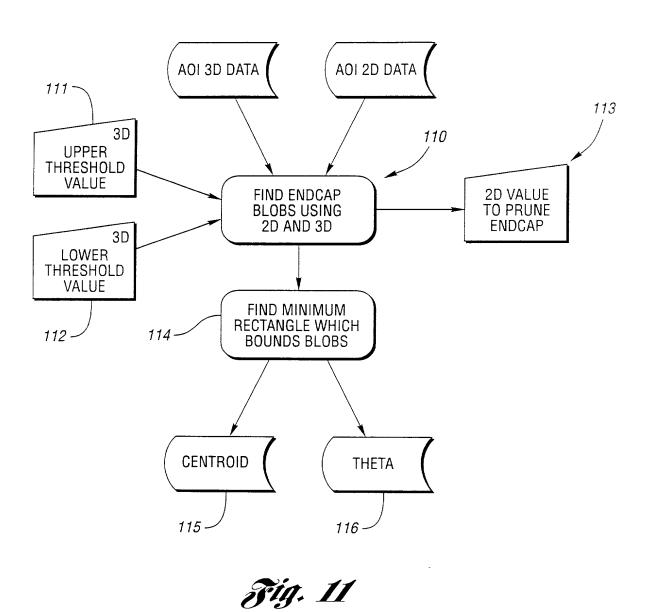


Fig. 106

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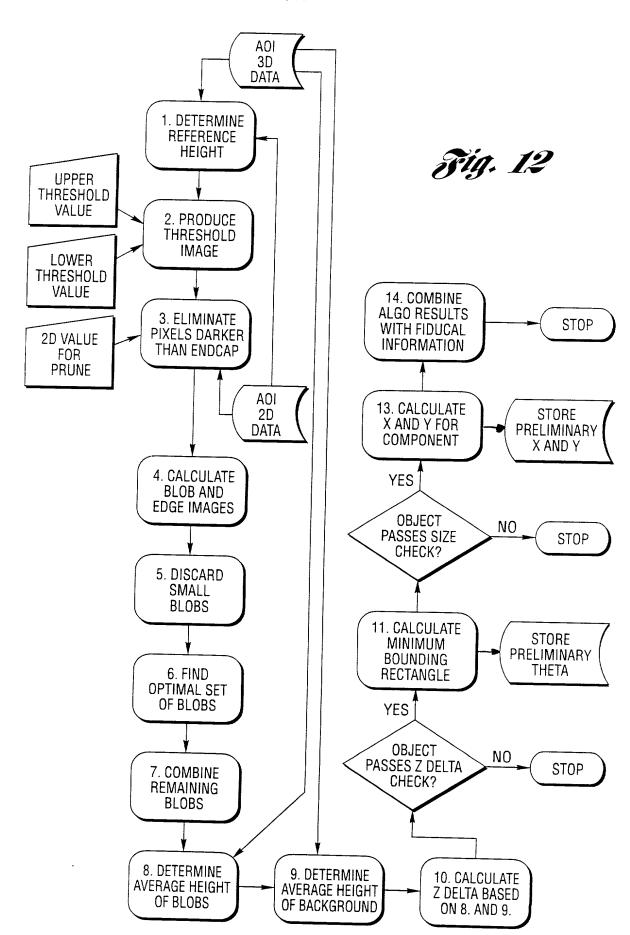
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8/9



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9/9



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